

Sobre a patente (*link* e autores):

Link UFSM:

<https://www.ufsm.br/app/uploads/sites/399/2022/06/RE-00465-SISTEMA-UNIVERSAL-INTERCAMBIAVEL-ACOPLAVEL-A-DISPOSITIVOS-DE-ORIENTACAO-ESPACIAL.pdf>

Todos abaixo marcados como ex-alunos cursavam graduação em Engenharia de Computação quando do desenvolvimento do trabalho.

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Sobre os trabalhos científicos (*links* e autores):

Links:

Periódicos

(2022) <http://dx.doi.org/10.1145/3561512> (Mobile Localization Techniques for Wireless Sensor Networks: Survey and Recommendations)

(2021) <http://dx.doi.org/10.1109/mdat.2021.3102884> (Dedicated Shapelet Distance Engine for Time-Series Classification)

Conferências

(2022) (ainda sem *link*) (Comparative Analysis of Hardware Implementations of a Convolutional Neural Network. In: 35th Symposium on Integrated Circuits and Systems Design)

(2020) <http://dx.doi.org/10.1109/SBCCI50935.2020.9189923> (Hardware Accelerator for Shapelet Distance Computation in Time-Series Classification)

(2020) <http://dx.doi.org/10.1109/iscas45731.2020.9180579> (Lightweight Cryptographic Instruction Set Extension on Xtensa Processor)

(2019) <http://dx.doi.org/10.1109/NEWCAS44328.2019.8961311> (Exploring the Training and Execution Acceleration of a Neural Network in a Reconfigurable General-purpose Processor for Embedded Systems)

(2019) <http://dx.doi.org/10.1109/newcas44328.2019.8961220> (GCoL - A General Co-simulator Applied to Wireless Sensor Networks and RTL Design)

(2018) <http://dx.doi.org/10.1109/ISCAS.2018.8351190> (An Efficient Single Core Flexible Processor Architecture for 4096-bit Montgomery Modular Multiplication and Exponentiation)

(2018) <http://dx.doi.org/10.1109/icecs.2018.8617840> (Exploring RSA Performance up to 4096-bit for Fast Security Processing on a Flexible Instruction Set Architecture Processor)

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